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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *YES LV*  
 This application is a CON of PCT/EP02/05253 05/13/2002

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *YES LV*  
 GERMANY 10123509.7 05/15/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/23/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY GERMANY	SHEETS DRAWING 4	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged  Examiner's Signature <i>LV</i> Initials <i>LV</i>				

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## TITLE

Method for the planarization of a semiconductor structure

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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